



INDIRA GANDHI DELHI TECHNICAL UNIVERSITY FOR WOMEN
Electronics and Communication Engineering Department

Kashmere Gate, Delhi-110006

Even Sem January 2023

FAD-04

Room No: **314-A**

w.e.f: 02/01/2023

M.TECH 2nd SEMESTER VLSI

	1	2	3	4	5	6	7	8
Days	9-10 am	10-11 am	11-12 am	12-1 pm	1-2pm	2-3 pm	3-4 pm	4-5 pm
Monday	ANNDL Lab		ANNDL	ADVLSI	Lunch	LPVLSI	AFD	
Tuesday	AFD Lab		DVD	ADVLSI	Lunch	LPVLSI	DVD	
Wednesday	DVD Lab		ANNDL	ADVLSI	Lunch	LPVLSI	AFD	
Thursday	LPVLSI Lab		AFD	ANNDL	Lunch	DVD	ADVLSI	
Friday								

**** M.Tech Lab room: Old HOD ECE office First Floor Electrical Block**

MVD-102	Digital VLSI Design (DVD) (Theory + Lab)	Mr Ejaz Lodhi
MVD-104	Advances in Emerging VLSI (ADVLSI) (Theory + Lab)	Prof Jasdeep Kaur Dhanoa
MVD-110	Analog filter Design (AFD) (Theory + Lab)	Dr Richa Yadav
MVD-120	Low Power VLSI Design (LPVLSI) (Theory + Lab)	Dr Vandana Niranjana
MVD-132	Artificial neural network and deep learning (ANNDL) (Theory + Lab)	Prof Nidhi Goel

HOD, ECE

(Dr. Kanchan Sharma)
Time Table Incharge



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Even Sem January 2023

FAD-04

Room No: **ONLINE MODE**

w.e.f: 02/01/2023

BTECH 8th SEMESTER ECE

	1	2	3	4	5	6	7	8
Days	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday				NNDL	Lunch	Major project	Major project	
Tuesday	ES Lab (GP-1) NNDL Lab (GP-2)		Embedded Systems	NNDL	Lunch	Wireless Sensor Networks	Major project	
Wednesday	ES Lab (GP-2) NNDL Lab (GP-1)		Embedded Systems	NNDL	Lunch	Wireless Sensor Networks	Major project	
Thursday			Embedded Systems	Major project	Lunch	Wireless Sensor Networks	Wireless Sensor Networks	
Friday								

BEC 402	Embedded Systems (ES) (Theory + Lab)	Prof. Vandana Niranjana
BIT 408	Neural Networks and Deep Learning (NNDL) (Theory + Lab)	Prof. Nidhi Goel
BEC 412	Wireless Sensor Networks (WSN) (Theory)	Dr. Greeshma Arya
BEC 452	Major Project	

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Even Sem January 2023

FAD-04

Room No: E-307

w.e.f.: 02/01/2023

BTECH 6th SEMESTER ECE-1

	1	2	3	4	5	6	7	8
Days	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday		Research Project	VLSI	DSP	MPMC Lab (GP-1) VLSI Lab (GP-2)		ITC	CC
Tuesday	ITC Lab (GP-1) MPMC Lab (GP-2)		MPMC	VLSI	Lunch	DSP	CC	Research Project
Wednesday	CC	ITC	ITC Lab (GP-2) VLSI Lab (GP-1)		DSP Lab (GP-2) CC Lab (GP-1)		MPMC	Research Project
Thursday	DSP Lab (GP-1) CC Lab (GP-2)		MPMC	DSP	Lunch	MM	MM	Research Project
Friday	VLSI	Research Project	ITC	Research Project	Lunch	Research Project	Research Project	Research Project

BEC-302	Digital Signal Processing (DSP) (Theory + Lab)	Dr. Kanchan Sharma
BEC-304	Information Theory & Coding (ITC) (Theory + Lab)	Ms. Astha Sharma +Dr. Greeshma Arya (GP-2 Lab only)
BEC-306	VLSI Design (Theory + Lab)	Dr Shobha Sharma
BEC-308	Microprocessors & Microcontrollers (MPMC) (Theory + Lab)	Dr Pankaj Gupta
BIT-304	Cloud Computing (CC) (Theory + Lab)	Dr Gaurav Indra
HMC-304	Marketing Management (MM) (Theory)	Prof Ashwni Kumar

HOD, ECE

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FAD-04

Room No: E-307

w.e.f.: 02/01/2023

BTECH 6th SEMESTER ECE-2

	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday		CC	MPMC Lab (GP-1) CC Lab (GP-2)		ITC	DSP	Research Project	
Tuesday	VLSI	DSP	DSP Lab (GP-2) VLSI Lab (GP-1)		MM	Research Project	Research Project	
Wednesday	DSP Lab (GP-1) MPMC Lab (GP-2)		MPMC	DSP	ITC	MPMC	Research Project	
Thursday	CC	MPMC	ITC Lab (GP-1) VLSI Lab (GP-2)		MM	Research Project	Research Project	
Friday	CC Lab (GP-1)		ITC	CC	VLSI	VLSI	ITC Lab (GP-2)	

BEC-302	Digital Signal Processing (DSP) (Theory + Lab)	Dr. Kanchan Sharma
BEC-304	Information Theory & Coding (ITC) (Theory + Lab)	Ms. Astha Sharma
BEC-306	VLSI Design (Theory + Lab)	Ms. Ramesha Suhail
BEC-308	Microprocessors & Microcontrollers (MPMC) (Theory + Lab)	Dr Pankaj Gupta
BIT-304	Cloud Computing (CC) (Theory + Lab)	Dr Greeshma Arya
HMC-304	Marketing Management (MM) (Theory)	Prof Ashwni Kumar

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FAD-04

Room No: **E-313**

w.e.f: **02/01/2023**

BTECH 4TH SEMESTER ECE-AI-1

	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday	OTDM Lab (GP-1)		LIC	Lunch	EMFT	Research Project	Research Project	Research Project
Tuesday	DCS	EMFT	Research Project	Lunch	OOP Lab (GP-2) LIC Lab (GP-1)		LIC	OTDM
Wednesday	OOP Lab (GP-1) LIC Lab (GP-2)		LIC	Lunch	DM	DM	DCS	Research Project
Thursday		OTDM	OTDM	Lunch	EMFT	OOP	OOP	Research Project
Friday	EMFT Lab (GP-1) DCS Lab (GP-2)		OTDM Lab (GP-2)		EMFT Lab (GP-2) DCS Lab (GP-1)		DCS	OOP

BEC-202	Linear Integrated Circuits (LIC) (Theory + Lab)	Ms. Vidhi Vishnoi
BEC-206	Electromagnetic Field Theory (EMFT) (Theory + Lab)	Ms. Neha Singh
BEC-210	Digital Communication Systems (DCS)	Dr Ritu Rani (Theory)+ Ms. Shambhavi Tiwari (Labs both group)
BAI-204	Optimization Techniques and Decision Making (OTDM) (Theory + Lab)	Mr Neeraj Juneja
BIT-204	Object Oriented Programming (OOP) (Theory + Lab)	Ms. Pooja Gambhir
HMC-202	Disaster Management (DM) (Theory + Lab)	Prof Ashwni Kumar

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Room No: E-313

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BTECH 4TH SEMESTER ECE-AI-2

	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday	DCS	EMFT	Research Project	LIC	Lunch	DM	DM	OTDM
Tuesday	OOP Lab (GP-1) LIC Lab (GP-2)		Research Project	EMFT	Lunch	OOP	OTDM	Research Project
Wednesday	DCS	EMFT	Research Project	OOP	Lunch	Research Project	Research Project	OTDM
Friday	LIC	Research Project	Research Project	OOP	OOP Lab (GP-2) LIC Lab (GP-1)		EMFT Lab (GP-2) DCS Lab (GP-1)	
Saturday	LIC	DCS	EMFT Lab (GP-1) DCS Lab (GP-2)		OTDM Lab (GP-1)		OTDM Lab (GP-2)	

BEC-202	Linear Integrated Circuits (LIC) (Theory + Lab)	Ms. Surbhi Bharti
BEC-206	Electromagnetic Field Theory (EMFT) (Theory + Lab)	Ms. Neha Singh
BEC-210	Digital Communication Systems (DCS)	Visiting Faculty (Theory) + Ms. Soonam (Labs both group)
BAI-204	Optimization Techniques and Decision Making (OTDM) (Theory + Lab)	Mr Neeraj Juneja
BIT-204	Object Oriented Programming (OOP) (Theory + Lab)	Ms. Nandini Sethi
HMC-202	Disaster Management (DM) (Theory + Lab)	Prof Ashwni Kumar

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Room No: E-309

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BTECH 4TH SEMESTER ECE

	1	2	3	4	5	6	7	8
Days	9-10 am	10-11 am	11-12 pm	12-1 pm	1-2 pm	2-3 pm	3-4 pm	4-5 pm
Monday	CS	LIC	EMFT	DM	LUNCH	OOP	OOP Lab (GP-1) CS Lab (GP-2)	
Tuesday	OOP	CS	EMFT	DM	LUNCH	Research Project	EMFT Lab (GP-1) DSD Lab (GP-2)	
Wednesday	EMFT	DSD	CS Lab (GP-1)		LUNCH	CS	EMFT Lab (GP-2) DSD Lab (GP-1)	
Thursday	LIC Lab (GP-1)		LIC	DSD	LUNCH	OOP	OOP Lab (GP-2)	
Friday	LIC Lab (GP-2)		LIC	DSD	LUNCH	Research Project	Research Project	Research Project

BEC-202	Linear Integrated Circuits (Theory + Lab)	Ms. Surbhi Bharti + Mr Ejaj Lodhi (GP-2 Lab)
BEC-204	Digital System Design (Theory + Lab)	Dr Shobha Sharma
BEC-206	Electromagnetic Field Theory (Theory + Lab)	Ms. Vaishali Kikan
BEC-208	Communication Systems (Theory + Lab)	Ms. Isha Malhotra
BIT-204	Object Oriented Programming (Theory + Lab)	Mr Neeraj Kohli (Theory) + Ms. Ankita (Lab –GP-1) + Ms. Nidhi Arora (Lab- GP-2)
HMC-202	Disaster Management (Theory + Lab)	Prof Ashwni Kumar

HOD, ECE

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